Lab 2:

A1. A 16-bit immediate value can hold possible values.

A2. The range is to

A3. 32 bits

A4. The load immediate (li) pseudo-instruction got turned into load unsigned int (lui) and logical or immediate (ori) instructions.

A5. lui: 0x3c01b357, ori: 0x3428c0de

C1. 0x1001000

C2. 0x1001002

C3. 0x1001004

C4. 2752554

C5. Runtime exception at 0x00400018: fetch address not aligned on word boundary 0x10010002.

C6. Value of y is 0.